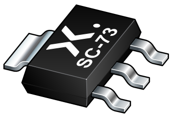


PBSS9110Z-Q

100 V, 1 A PNP low VCEsat transistor

29 July 2025 Product data sheet

1. General description

PNP low V_{CEsat} transistor in a SOT223 (SC-73) small Surface-Mounted Device (SMD) plastic package.

NPN complement: PBSS8110Z-Q

2. Features and benefits

- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- High collector current gain (h_{FE}) at high I_C
- High efficiency due to less heat generation
- Smaller required Printed-Circuit Board (PCB) area than for conventional transistors
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- High-voltage DC-to-DC conversion
- High-voltage MOSFET gate driving
- High-voltage motor control
- High-voltage power switches (e.g. motors, fans)
- Automotive applications

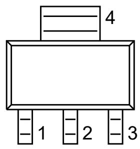
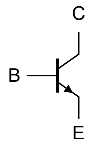
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base		-	-	-100	V
I_C	collector current			-	-	-1	A
I_{CM}	peak collector current	single pulse; $t_p \leq$ 1 ms		-	-	-3	A
R_{CEsat}	collector-emitter saturation resistance	I_C = -1 A; I_B = -100 mA; pulsed; $t_p \leq$ 300 μs; $\delta \leq$ 0.02; T_{amb} = 25 °C		-	170	320	mΩ

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base	 SC-73 (SOT223)	
2	C	collector		
3	E	emitter		
4	C	collector		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS9110Z-Q	SC-73	plastic, surface-mounted package with increased heatsink; 4 leads; 2.3 mm pitch; 6.5 mm x 3.5 mm x 1.65 mm body	SOT223

7. Marking

Table 4. Marking codes

Type number	Marking code
PBSS9110Z-Q	PB9110

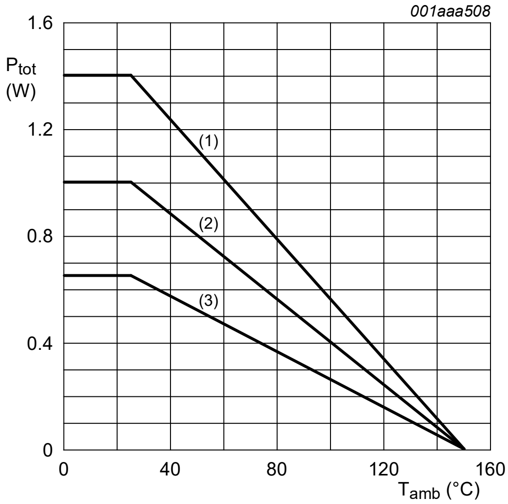
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-120	V
V_{CEO}	collector-emitter voltage	open base		-	-100	V
V_{EBO}	emitter-base voltage	open collector		-	-5	V
I_C	collector current			-	-1	A
I_{CM}	peak collector current	single pulse; $t_p \leq$ 1 ms		-	-3	A
I_B	base current			-	-0.3	A
P_{tot}	total power dissipation	$T_{amb} \leq$ 25 °C	[1]	-	0.65	W
			[2]	-	1	W
			[3]	-	1.4	W
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-65	150	°C
T_{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm^2.
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm^2.



(1) FR4 PCB, mounting pad for collector 6 cm^2
(2) FR4 PCB, mounting pad for collector 1 cm^2
(3) FR4 PCB, standard footprint

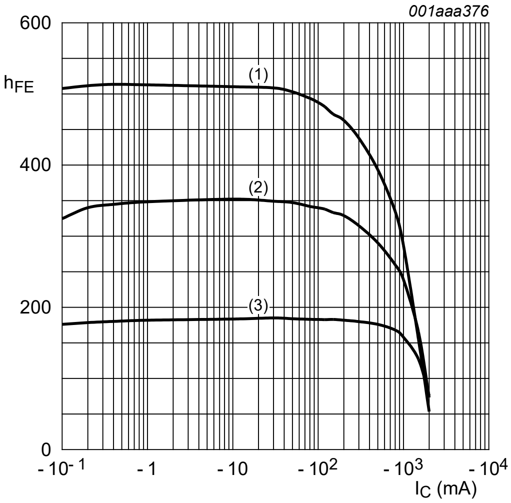
Fig. 1. Power derating curves

9. Thermal characteristics

Table 6. Thermal characteristics

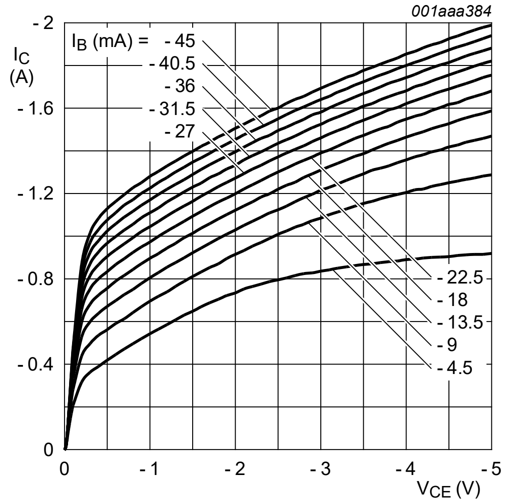
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	192	K/W
			[2]	-	-	125	K/W
			[3]	-	-	89	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	17	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm^2.
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm^2.



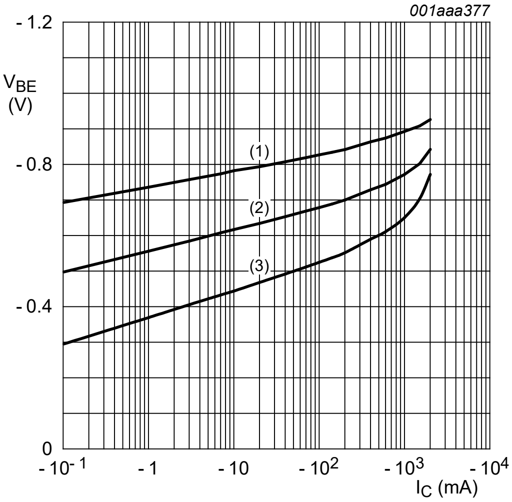
V_{CE} = −10 V
(1) T_{amb} = 100 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = −55 °C

Fig. 2. DC current gain as a function of collector current; typical values



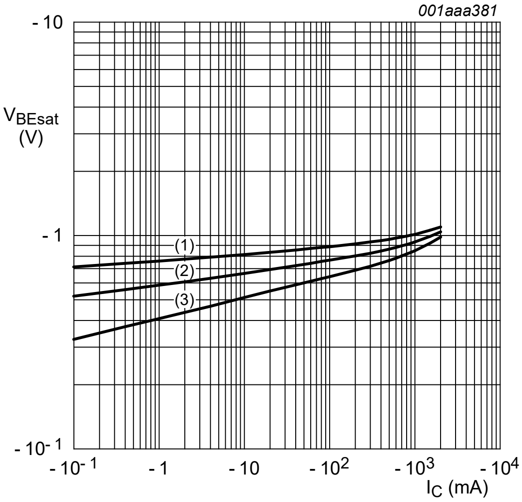
T_{amb} = 25 °C

Fig. 3. Collector current as a function of collector-emitter voltage; typical values



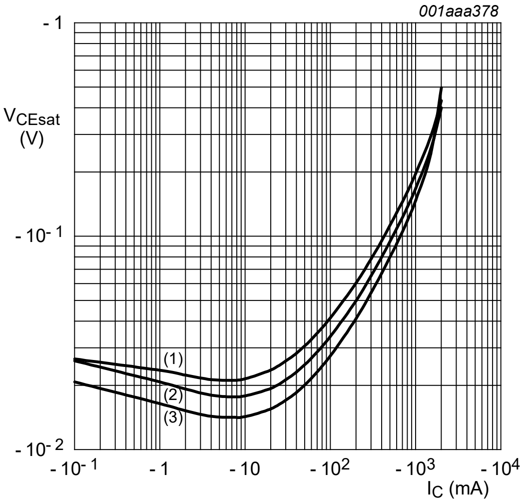
V_{CE} = −10 V
(1) T_{amb} = −55 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = 100 °C

Fig. 4. Base-emitter voltage as a function of collector current; typical values



I_C/I_B = 10
(1) T_{amb} = −55 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = 100 °C

Fig. 5. Base-emitter saturation voltage as a function of collector current; typical values



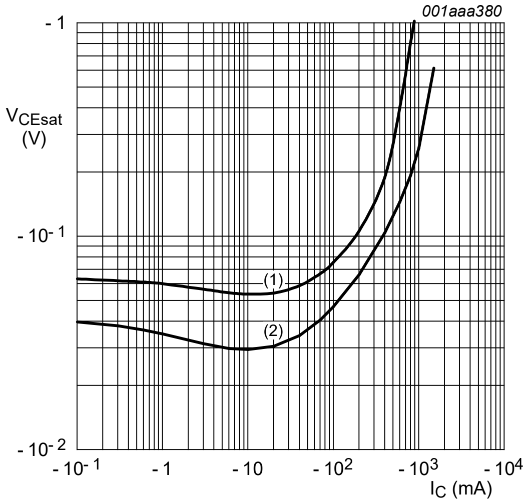
$I_C/I_B = 10$

(1) $T_{amb} = 100$ °C

(2) $T_{amb} = 25$ °C

(3) $T_{amb} = -55$ °C

Fig. 6. Collector-emitter saturation voltage as a function of collector current; typical values

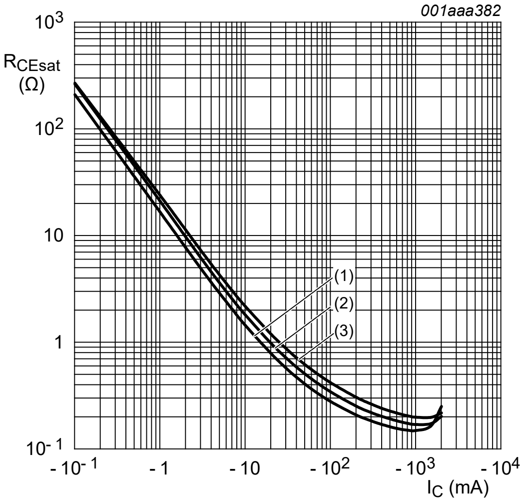


$T_{amb} = 25$ °C

(1) $I_C/I_B = 50$

(2) $I_C/I_B = 20$

Fig. 7. Collector-emitter saturation voltage as a function of collector current; typical values



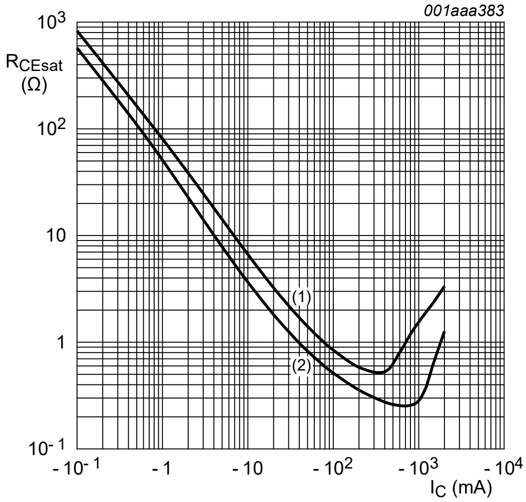
$I_C/I_B = 10$

(1) $T_{amb} = -55$ °C

(2) $T_{amb} = 25$ °C

(3) $T_{amb} = 100$ °C

Fig. 8. Collector-emitter saturation resistance as a function of collector current; typical values



$T_{amb} = 25$ °C

(1) $I_C/I_B = 50$

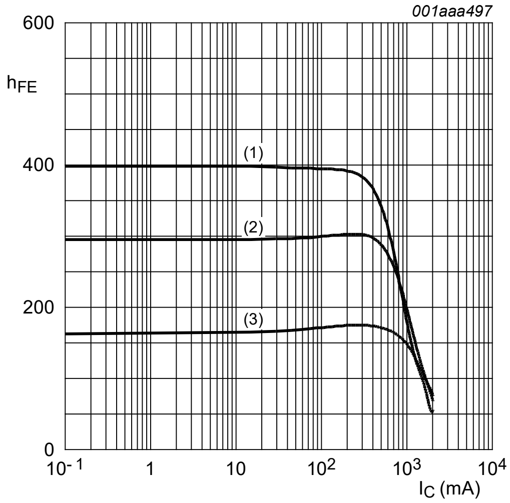
(2) $I_C/I_B = 20$

Fig. 9. Collector-emitter saturation resistance as a function of collector current; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
I_{CBO}	collector-base cut-off current	V_{CB} = -80 V; I_E = 0 A; T_{amb} = 25 °C		-	-	-100	nA
		V_{CB} = -80 V; I_E = 0 A; T_j = 150 °C		-	-	-50	µA
I_{CES}	collector-emitter cut-off current	V_{CE} = -80 V; V_{BE} = 0 V; T_{amb} = 25 °C		-	-	-100	nA
I_{EBO}	emitter-base cut-off current	V_{EB} = -4 V; I_C = 0 A; T_{amb} = 25 °C		-	-	-100	nA
h_{FE}	DC current gain	V_{CE} = -5 V; I_C = -1 mA; T_{amb} = 25 °C		150	-	-	
		V_{CE} = -5 V; I_C = -250 mA; T_{amb} = 25 °C		150	-	-	
		V_{CE} = -5 V; I_C = -0.5 A; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		150	-	450	
		V_{CE} = -5 V; I_C = -1 A; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		125	-	-	
V_{CEsat}	collector-emitter saturation voltage	I_C = -250 mA; I_B = -25 mA; T_{amb} = 25 °C		-	-	-120	mV
		I_C = -500 mA; I_B = -50 mA; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	-	-180	mV
		I_C = -1 A; I_B = -100 mA; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	-	-320	mV
R_{CEsat}	collector-emitter saturation resistance			-	170	320	mΩ
V_{BEsat}	base-emitter saturation voltage			-	-	-1.1	V
V_{BEon}	base-emitter turn-on voltage	V_{CE} = -5 V; I_C = -1 A; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	-	-1	V
t_d	delay time	V_{CC} = -10 V; I_C = 0.5 A; I_{Bon} = 0.025 A; I_{Boff} = -0.025 A; T_{amb} = 25 °C		-	20	-	ns
t_r	rise time	I_C = -0.5 A; T_{amb} = 25 °C		-	60	-	ns
t_{on}	turn-on time	I_{Bon} = -0.025 A; I_{Boff} = 0.025 A; T_{amb} = 25 °C		-	80	-	ns
t_s	storage time	T_{amb} = 25 °C		-	290	-	ns
t_f	fall time			-	120	-	ns
t_{off}	turn-off time			-	410	-	ns
f_T	transition frequency	V_{CE} = -10 V; I_C = -50 mA; f = 100 MHz; T_{amb} = 25 °C		100	-	-	MHz
C_c	collector capacitance	V_{CB} = -10 V; I_E = 0 A; i_e = 0 A; f = 1 MHz; T_{amb} = 25 °C		-	-	17	pF



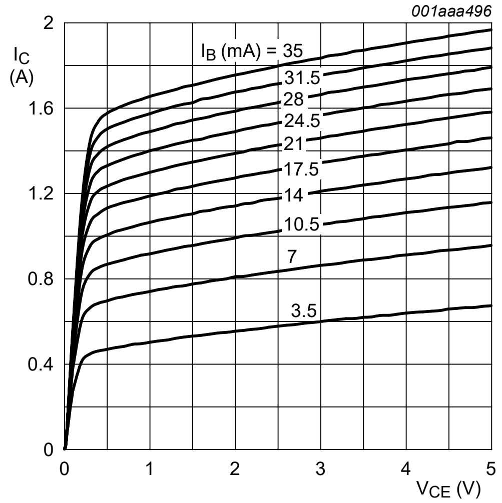
$V_{CE} = 10$ V

(1) $T_{amb} = 100$ °C

(2) $T_{amb} = 25$ °C

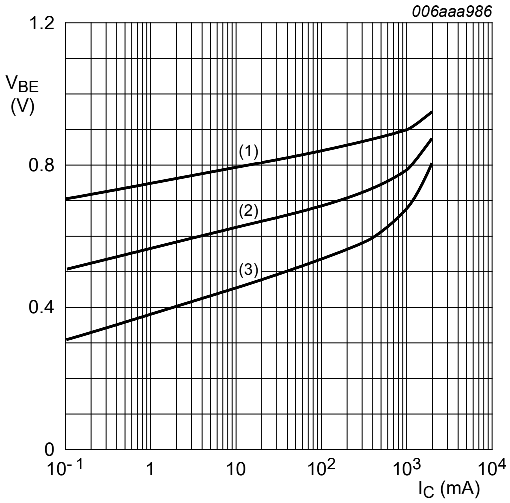
(3) $T_{amb} = -55$ °C

Fig. 10. DC current gain as a function of collector current; typical values



$T_{amb} = 25$ °C

Fig. 11. Collector current as a function of collector-emitter voltage; typical values



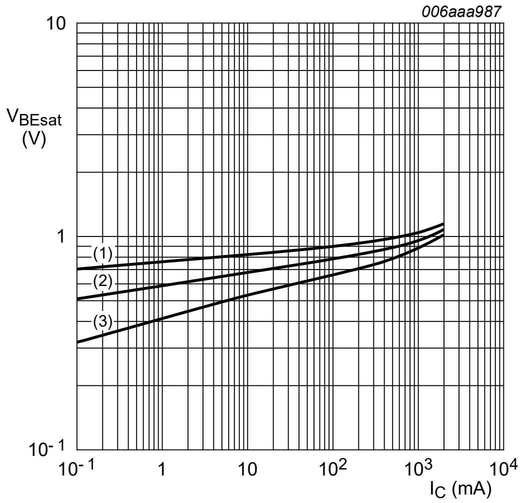
$V_{CE} = 10$ V

(1) $T_{amb} = -55$ °C

(2) $T_{amb} = 25$ °C

(3) $T_{amb} = 100$ °C

Fig. 12. Base-emitter voltage as a function of collector current; typical values



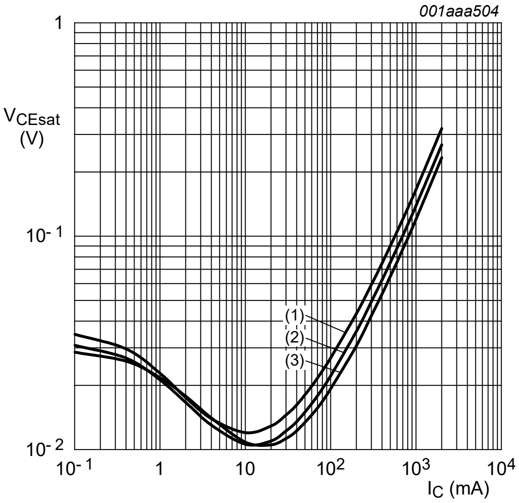
$I_C / I_B = 10$ V

(1) $T_{amb} = -55$ °C

(2) $T_{amb} = 25$ °C

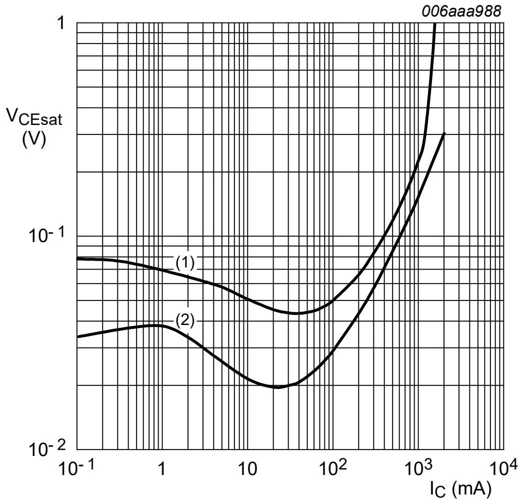
(3) $T_{amb} = 100$ °C

Fig. 13. Base-emitter saturation voltage as a function of collector current; typical values



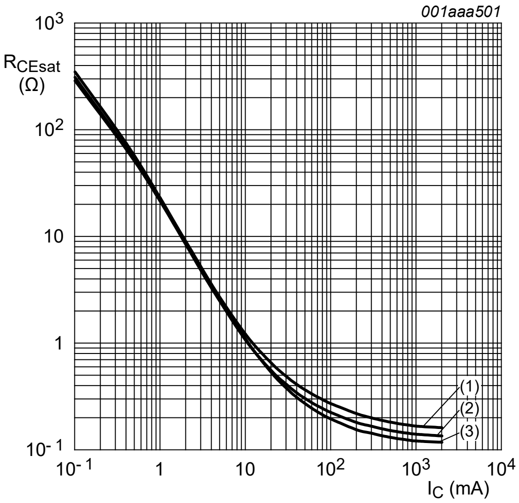
$I_C/I_B = 10$
(1) T_{amb} = 100 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = -55 °C

Fig. 14. Collector-emitter saturation voltage as a function of collector current; typical values



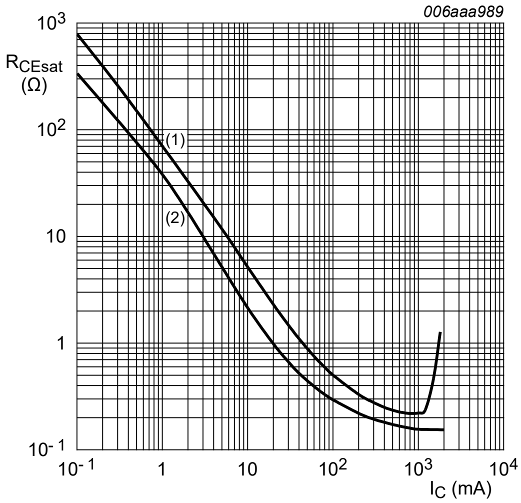
$T_{amb} = 25$ °C
(1) $I_C / I_B = 50$
(2) $I_C / I_B = 20$

Fig. 15. Collector-emitter saturation voltage as a function of collector current; typical values



$I_C/I_B = 10$
(1) T_{amb} = 100 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = -55 °C

Fig. 16. Collector-emitter saturation resistance as a function of collector current; typical values



$T_{amb} = 25$ °C
(1) $I_C / I_B = 50$
(2) $I_C / I_B = 20$

Fig. 17. Collector-emitter saturation resistance as a function of collector current; typical values

11. Test information

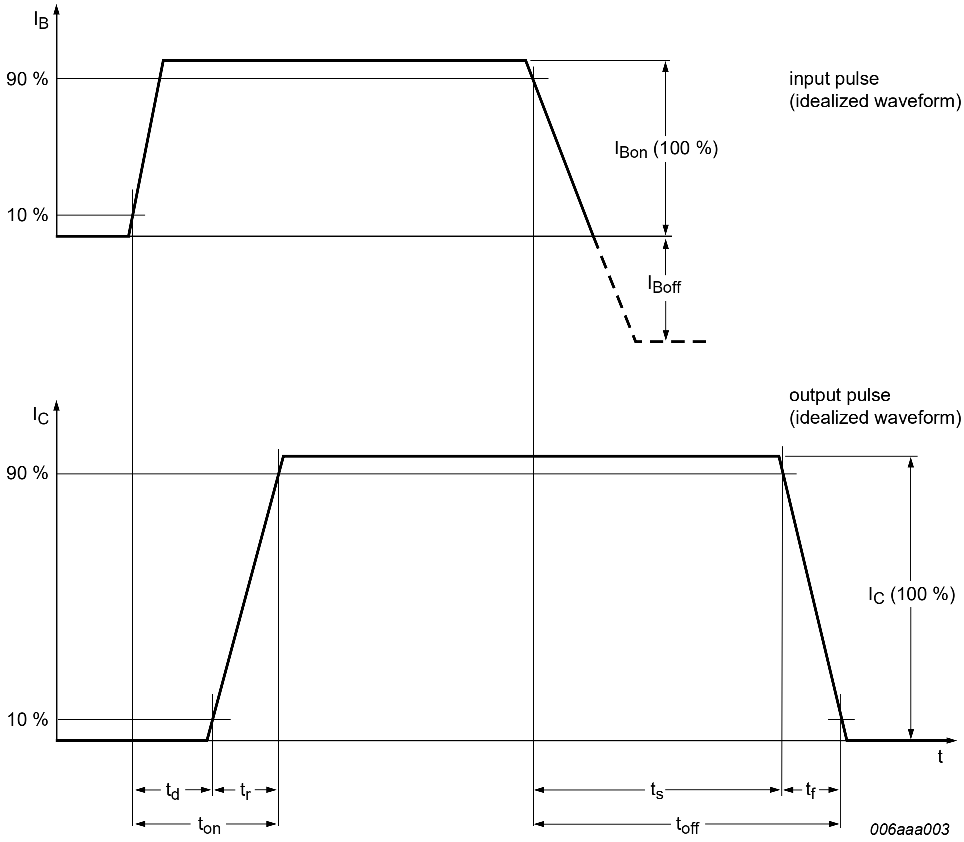


Fig. 18. Transistor switching time definition

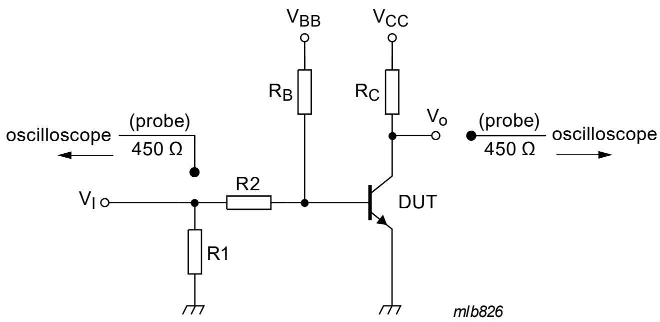


Fig. 19. Test circuit for switching times

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

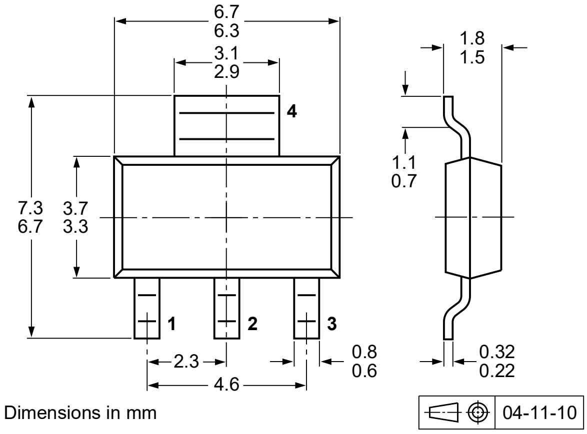


Fig. 20. Package outline SC-73 (SOT223)

13. Soldering

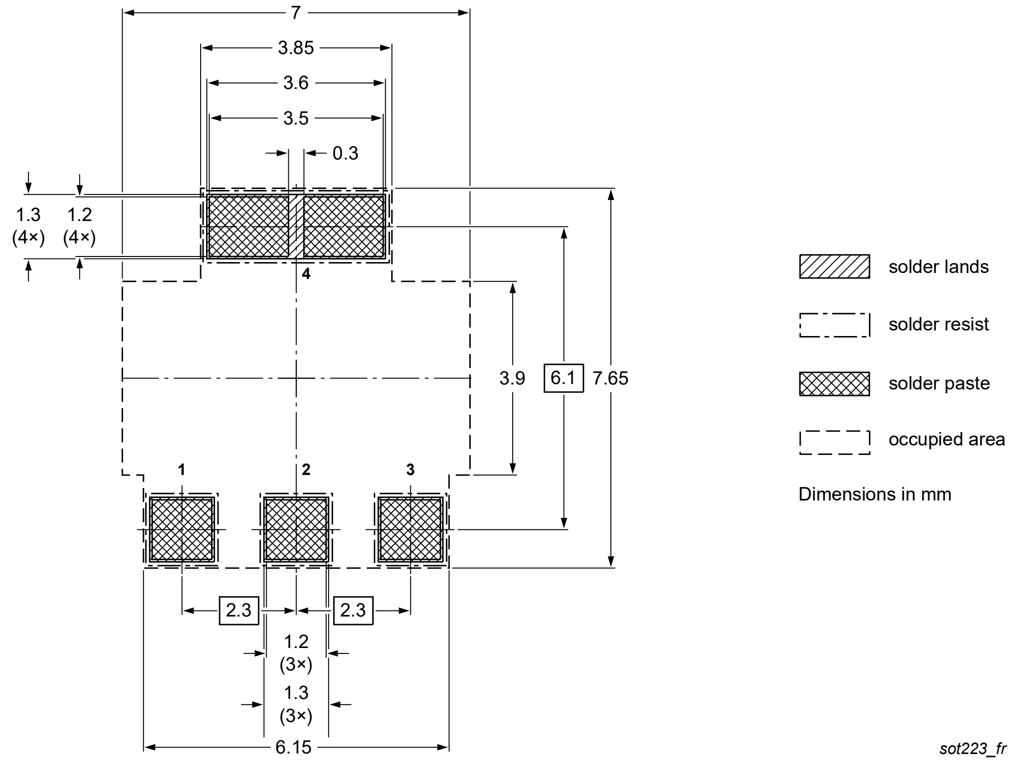


Fig. 21. Reflow soldering footprint for SC-73 (SOT223)

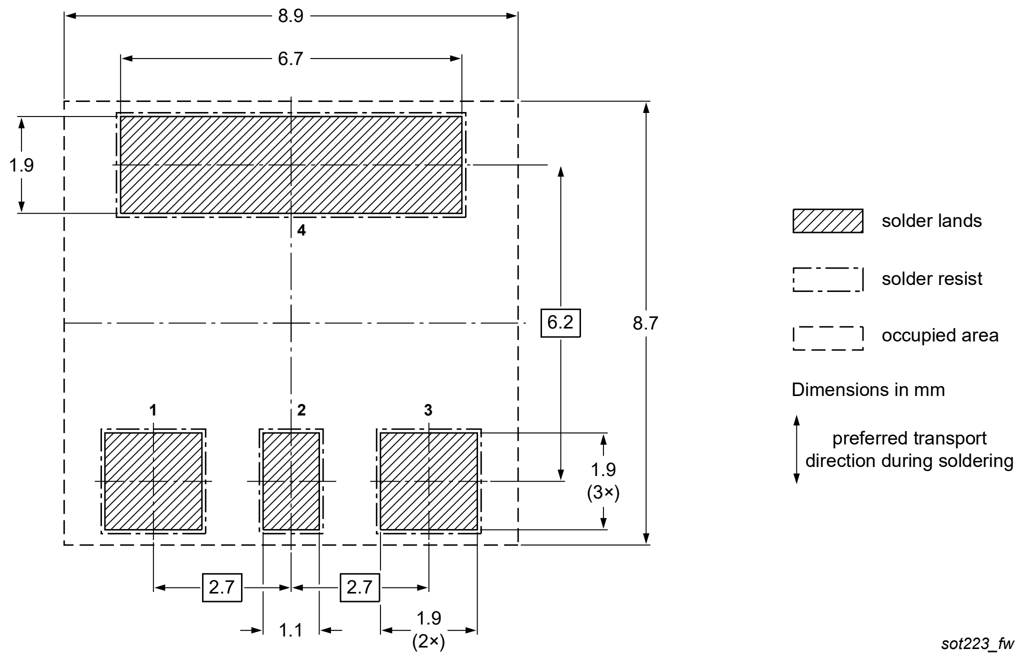


Fig. 22. Wave soldering footprint for SC-73 (SOT223)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS9110Z-Q v.1	20250729	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term 'short data sheet' is explained in section "Definitions".
[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at https://www.nexperia.com.

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Contents

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Date of release: 29 July 2025